

Substitute for Form 1449 A & B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/897,732
(use as many sheets as necessary)				Confirmation Number	8188
				Filing Date	July 03, 2001
				First Named Inventor	Jae-Hong KIM
				Art Unit	2631
				Examiner Name	Not Yet Assigned
Sheet 1		of 1	Attorney Docket Number Q63316		

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U.S. PATENT DOCUMENTS					
Examiner Initials* TRADEMARK OFFICE	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ³ (if known)		
CF		US 5,928,377	A	7/27/1999	Doran et al.
		US			

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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴			
CF		EP	0817440	A2	1/7/1998	Matsushita Electric Industrial Co., Ltd.
CF		EP	1187342	A2	3/13/2002	Sony Corporation

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
CF	3	"3 RD GENERATION PARTNERSHIP PROJECT (3GPP); TECHNICAL SPECIFICATION GROUP RADIO ACCESS NETWORK MULTIPLEXING AND CHANNEL CODING (FDD) (3G TS 25.212 VERSION 3.0.0)" 3G TS 25.212 V3.0.0,XX,XX, October 1999, XP002149187	
CF	4	Y. OKUMURA et al., "VARIABLE-RATE DATA TRANSMISSION WITH BLIND RATE DETECTION FOR COHERENT DS-CDMA MOBILE RADIO", IEICE TRANSACTIONS ON COMMUNICATIONS, INSTITUTE OF ELECTRONICS INFORMATION AND COMM. ENG. TOKYO, JP, vol. E81-B, no. 7, July 1, 1998, pages 1365-1372, XP000790169	
CF	5	R. CYPHER et al., "GENERALIZED TRACE-BACK TECHNIQUES FOR SURVIVOR MEMORY MANAGEMENT IN THE VITERBI ALGORITHM", JOURNAL OF VLSI SIGNAL PROCESSING SYSTEMS FOR SIGNAL, IMAGE, AND VIDEO TECHNOLOGY, KLUWER ACADEMIC PUBLISHERS, DORDRECHT, NL, vol. 5, no. 1, 1993, pages 85-94, XP000364288	
CF	6	B. SBIM et al., "PIPELINED VLSI ARCHITECTURE OF THE VITERBI DECODER FOR IMT-2000" CONFERENCE PROCEEDINGS ARTICLE, vol. 1A, December 5, 1999, pages 158-162, XP010373290	

Examiner Signature	Chet M. J.	Date Considered	9/15/04
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¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to indicate here if English language Translation is attached.

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1